a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first signal lines over said substrate;

a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;

a smoothing film comprising an organic resin formed over said switching elements;

a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and

a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and

a second driver circuit electrically connected to said plurality of first signal lines, wherein said second driver circuit comprises second thin film transistors formed over said substrate.

3 5. (Amended) The display device according to claim 1 wherein at least one of said first and second thin film transistors has a channel region comprising semi-amorphous silicon.

4 6. (Amended) The display device according to claim 1 wherein each of said first and second thin film transistors has a top-gate structure.

6 8. (Amended) A display device comprising:

a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first signal lines over said substrate;

a plurality of switching elements formed at each intersection of said first and

second signal lines, each of said switching elements comprising at least one first thin film transistor, wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;

a smoothing film comprising an organic resin formed over said switching elements;

a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and

a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and

a second driver circuit electrically connected to said plurality of first signal lines, wherein said second driver circuit comprises second thin film transistors formed over said substrate,

wherein said first driver circuit is connected to said substrate through a tape automated bonding process.

9 ~~13~~. (Amended) The display device according to claim ~~8~~ 6 wherein at least one of said first and second thin film transistors has a channel region comprising semi-amorphous silicon.

10 ~~14~~. (Amended) The display device according to claim ~~8~~ 6 wherein each of said first and second thin film transistors has a top-gate structure.

17 ~~21~~. (Amended) A display device comprising:

a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first signal lines over said substrate;

a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor, wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality ~~i~~ of first signal lines and an impurity region of said first thin film transistor

is electrically connected to one of said plurality of second signal lines;

a smoothing film comprising an organic resin formed over said switching elements;

a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and

a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and

a second driver circuit electrically connected to said plurality of first signal lines wherein said second driver circuit comprises second thin film transistors formed over said substrate,

wherein said IC chip is mounted over said substrate.

18 22. (Amended) The display device according to claim 21 17 wherein each of said first and second thin film transistors has a top-gate structure.

20 26. (Amended) The display device according to claim 21 17 wherein at least one of said first and second thin film transistors has a channel region comprising semi-amorphous silicon.

Please add new claims 27-55 as follows:

21 --27. A display device comprising:

a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first signal lines over said substrate;

a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;

an organic resin film formed over said switching elements;

a plurality of pixel electrodes formed over said organic resin film and electrically

connected to said switching elements through contact holes formed in said organic resin film; and

a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and

a second driver circuit electrically connected to said plurality of first signal lines, wherein said second driver circuit comprises second thin film transistors formed over said substrate.

22 28. The display device according to claim 21 wherein said organic resin film comprises polyimide.

23 29. The display device according to claim 21 wherein each of said first and second thin film transistors has a top-gate structure.

24 30. A display device comprising:

a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first signal lines over said substrate;

a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one thin film transistor;

an organic resin film formed over said switching elements;

a plurality of pixel electrodes formed over said organic resin film and electrically connected to said switching elements through contact holes formed in said organic resin film; and

a driver circuit comprising at least one IC chip for driving said switching elements,

wherein said IC chip is formed on a flexible support and said flexible support is connected to said substrate.

25 31. The display device according to claim 24 wherein said organic resin film

comprises polyimide.

26 32. The display device according to claim 30 wherein said driver circuit includes a first driver circuit including said IC chip and electrically connected to said plurality of second signal lines and a second driver circuit comprising second thin film transistors and electrically connected to said plurality of first signal lines.

27 33. A display device comprising:

a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first lines over said substrate;

a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor, wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;

an organic resin film formed over said switching elements;

a plurality of pixel electrodes formed over said organic resin film and electrically connected to said switching elements through contact holes formed in said organic resin film; and

a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and

a second driver circuit electrically connected to said plurality of first signal lines wherein said second driver circuit comprises second thin film transistors formed over said substrate,

wherein said IC chip is mounted over said substrate.

28 34. The display device according to claim 33 wherein said organic resin film comprises polyimide.

29 35. The display device according to claim 27 wherein each of said first and second thin film transistors has a top-gate structure.

30 36. A display device comprising:

a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first signal lines over said substrate;

a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;

a smoothing film formed over said switching elements;

a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and

a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and

a second driver circuit electrically connected to said plurality of first signal lines, wherein said second driver circuit comprises second thin film transistors formed over said substrate.

31 37. The display device according to claim 30 wherein each of said first and second thin film transistors has a top-gate structure.

32 38. A display device comprising:

a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first signal lines over said substrate;

a plurality of switching elements formed at each intersection of said first and

second signal lines, each of said switching elements comprising at least one thin film transistor;

a smoothing film formed over said switching elements;

a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and

a driver circuit comprising at least one IC chip for driving said switching elements,

wherein said IC chip is formed on a flexible support and said flexible support is connected to said substrate.

33 ~~39.~~ The display device according to claim ~~38~~ 32 wherein said driver circuit includes a first driver circuit including said IC chip and electrically connected to said plurality of second signal lines and a second driver circuit comprising second thin film transistors and electrically connected to said plurality of first signal lines.

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34 ~~40.~~ A display device comprising:

a substrate;

a plurality of first signal lines formed over said substrate;

a plurality of second signal lines extending across said first signal lines over said substrate;

a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor, wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;

a smoothing film formed over said switching elements;

a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and

a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and

a second driver circuit electrically connected to said plurality of first signal lines wherein said second driver circuit comprises second thin film transistors formed over said

substrate,

wherein said IC chip is mounted over said substrate.

35 41. The display device according to claim 11 wherein said driver circuit includes a first driver circuit including said IC chip and electrically connected to said plurality of second signal lines and a second driver circuit comprising second thin film transistors and electrically connected to said plurality of first signal lines.

36 42. The display device according to claim 1 wherein each of said first and second driver circuits comprises a shift register circuit.

37 43. The display device according to claim 6 wherein each of said first and second driver circuits comprises a shift register circuit.

38 44. The display device according to claim 17 wherein each of said first and second driver circuits comprises a shift register circuit.

39 45. The display device according to claim 21 wherein each of the first and second driver circuits comprises a shift register circuit.

40 46. The display device according to claim 27 wherein each of the first and second driver circuits comprises a shift register circuit.

41 47. The display device according to claim 30 wherein each of the first and second driver circuits comprises a shift register circuit.

42 48. The display device according to claim 34 wherein each of said first and second driver circuits comprises a shift register circuit.

43 49. The display device according to claim 1 wherein said first driver circuit comprises a latch circuit.